

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:	Sheu, <i>et al.</i>	Docket No.:	TSM03-0140
Serial No.:	10/619,828	Art Unit:	2891
Filed:	July 15, 2003	Examiner:	Amar Movva
Title:	Self-Aligned MOSFET having an Oxide Region below the Channel		

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

The Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed forms PTO/SB/08a and 08b, which may be considered material to the examination of the above-identified application. Copies of the U.S. Patents and Publications cited are not being submitted. However, the Applicants have included copies of the non-patent literature.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No. 50-1065 of the below mentioned firm.

Respectfully submitted,

Apr. 23, 2008

Date

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